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## JAPANESE PATENT OFFICE

## PATENT ABSTRACTS OF JAPAN

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(71)Applicant: CANON INC

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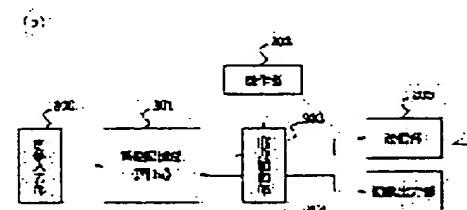
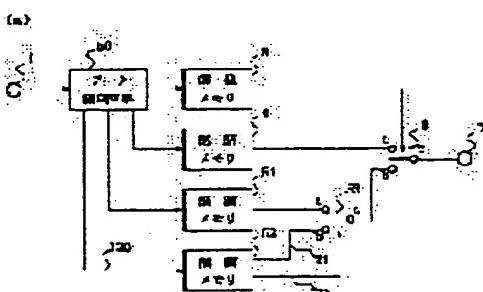
(72)Inventor: ISHIKAWA TAKASHI

## (54) IMAGE PROCESSOR

(57)Abstract:

**PROBLEM TO BE SOLVED:** To efficiently express images for which color line images and halftone images coexist with high image quality by turning bit map data for indicating the line images, the coloring data and image data for indicating the halftone images into the image data for constituting one screen.

**SOLUTION:** For data inputted from the input terminal 1 of an image storage part 201, header information is interpreted in a data identification circuit 50, the bit map data of a text are stored in a resolution memory 3, the gradation (color) data of the text are stored in a gradation memory 4, a background color is stored in the gradation memory 51, and the image data provided with a halftone are stored in the gradation memory 52 respectively. The



read of the memories is controlled, so as to output the data corresponding to respective pixels successively from the head of a page from the resolution memory 3 and the gradation memories 4, 51 and 52 corresponding to synchronization signals from a printer side. To the control terminal of a selector 53, image area signals 122 outputted from the gradation memory 52 are inputted. The data for the changeover of the gradation memories 4 and 5 are 1-bit data for holding a resolution stored in the resolution memory 3.

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**LEGAL STATUS**

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[Date of sending the examiner's decision of rejection]

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[Date of extinction of right]

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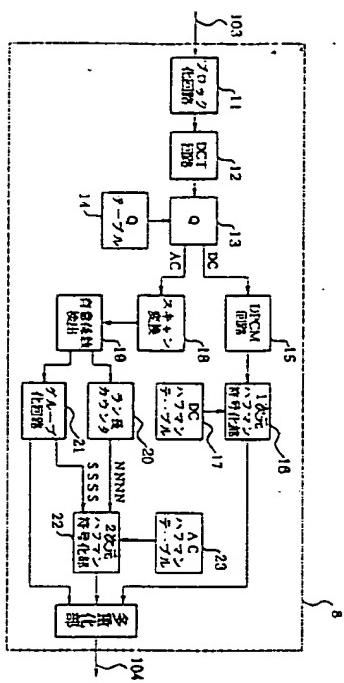
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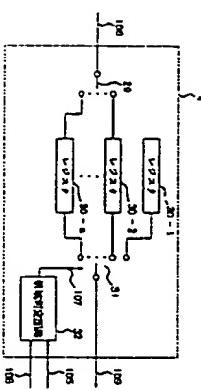




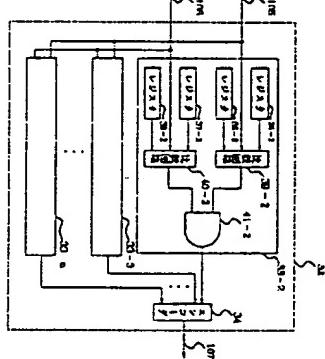
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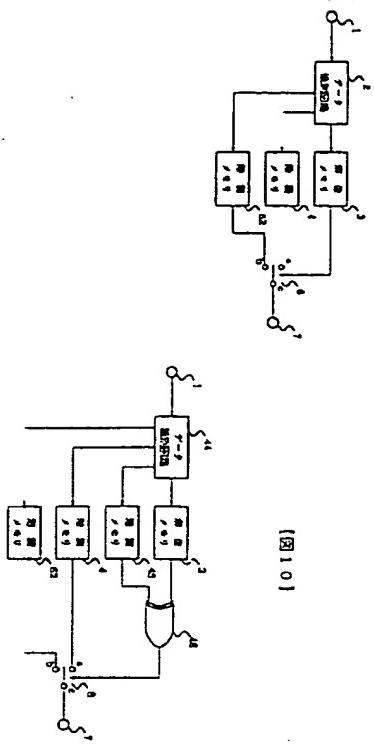
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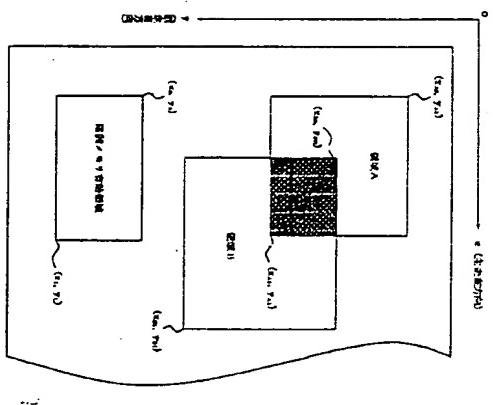
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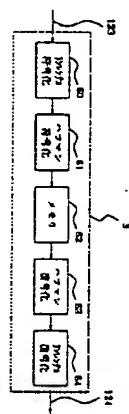
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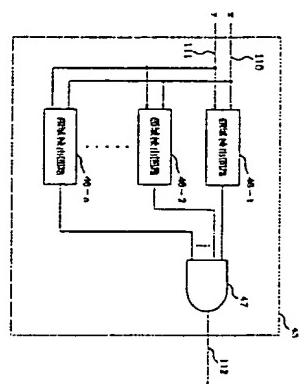
[图7]



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